

IN THE SPECIFICATION

Please replace the paragraph starting on page 1, line 13 with the following paragraph:

The present application may relate to ~~co-pending application Serial No. \_\_\_\_\_, (Attorney Docket No. 0325.00386) filed December 21, 2000 U.S. Patent No. 6,366,145, Serial No. \_\_\_\_\_, (Attorney Docket No. 0325.00387) filed December 21, 2000 U.S. Patent No. 6,417,698, Serial No. \_\_\_\_\_, (Attorney Docket No. 0325.00389) filed December 22, 2000 U.S. Patent No. 6,711,226, Serial No. \_\_\_\_\_, (Attorney Docket No. 0325.00390) filed December 22, 2000 U.S. Patent No. 6,535,023, and co-pending application Serial No. \_\_\_\_\_ 09/747,188, (Attorney Docket No. 0325.00391) filed December 22, 2000, which are each hereby incorporated by reference in their entirety.~~

Please replace the paragraph starting on page 18, line 8 with the following paragraph:

Referring to FIG. 6, a method (or process) 200 is shown. The method 200 generally comprises a decision state 202, a state 204, a state 206, a state 208, a decision state 210, a decision state 212, a decision state 214, a decision state 216, a state 218 and a state 220. The decision state 202 generally determines if a data edge is present. If a data edge is not present, the decision state 202 continues to check for such a condition. If a data edge

is present, the state 204 determines a relative polarity and phase-offset magnitude for the data and clock. The state 206 adds the polarity and magnitude to a previously accumulated value stored in the state 208. Next, the state 208 stores the next accumulated value from the state 206. The decision state 210 determines if a high bandwidth condition has occurred. If such high bandwidth condition has occurred, the state 212 determines the polarity from the state 204. If the polarity is positive, the state 218 switches clock counter clockwise and returns to the state ~~212~~ 202. If the state 212 determines that the polarity from the state 204 is negative, the state 216 determines if the magnitude in the state 208 is less than -M. If ~~so~~ no, the method 200 returns to the state 202. If the magnitude of the value of the state 208 is less than -M, the state 220 switches the clocks clockwise and returns to the state 202.

Page 20, after line 15, please add the following new paragraph:

The present invention may be implemented as a method of synchronizing a clock signal to a data signal, comprising the steps of (A) upon power-up, performing said synchronization with a high bandwidth system, (B) after a predetermined amount of time, performing said synchronization with a low bandwidth system and (C) adding a first value to a second value to produce a third value.

The second value represents a position of a second edge of the data signal. The present invention may also be implemented as an apparatus for synchronization of a clock signal to a data signal comprising a detector configured to synchronize with a high bandwidth system. The detector may be configured after a predetermined amount of time to perform the synchronization with a low bandwidth system. The detector may comprise an accumulator that adds a first value to a second value to produce a third value. The second value may represent a position of a second edge of the data signal.